WITICK Multilayer Ceramic Chip Capacitors

C3225JB0J686M200AC



TDK item description C3225JB0J686MT****

Applications	Commercial Grade	
Feature	General General (Up to 50V)	
Series	C3225 [EIA 1210]	
Status	Production	



	Size	
Length(L)	3.20mm ±0.40mm	
Width(W)	2.50mm ±0.30mm	
Thickness(T)	2.00mm ±0.20mm	
Terminal Width(B)	0.20mm Min.	
Terminal Spacing(G)		
Recommended Land Pattern (PA)	2.00mm to 2.40mm	
Recommended Land Pattern (PB)	1.00mm to 1.20mm	
Recommended Land Pattern (PC)	1.90mm to 2.50mm	

Electrical Characteristics		
Capacitance	68μF ±20%	
Rated Voltage	6.3VDC	
Temperature Characteristic	JB(±10%)	
Dissipation Factor (Max.)	15%	
Insulation Resistance (Min.)	1ΜΩ	

Other	
Soldering Method	Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	1000pcs

! Images are for reference only and show exemplary products.

! This PDF document was created based on the data listed on the TDK Corporation website.

! All specifications are subject to change without notice.

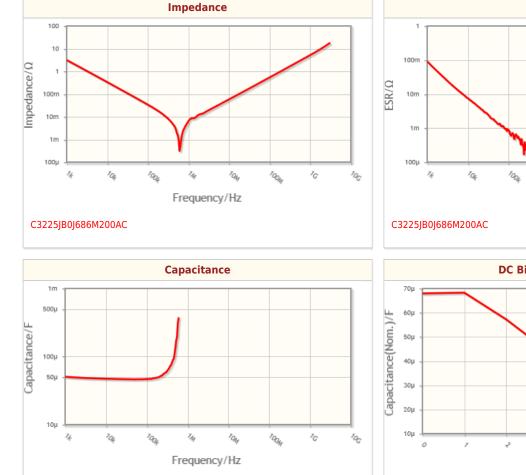
C3225JB0J686M200AC



ζÇ

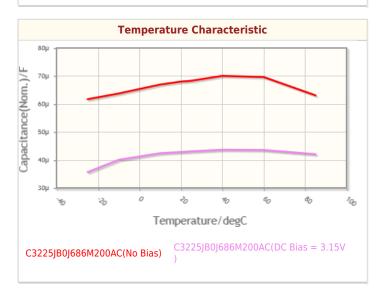
m.

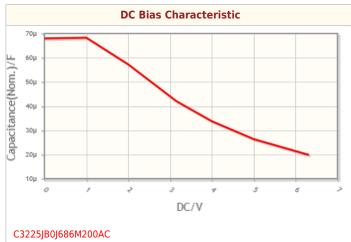
'n.



Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)





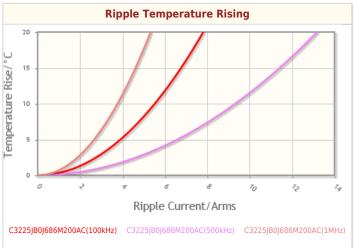


ESR

74

Frequency/Hz

Iq.



! Images are for reference only and show exemplary products.

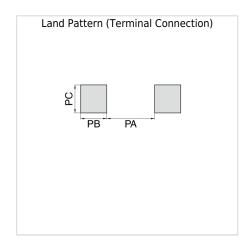
! This PDF document was created based on the data listed on the TDK Corporation website.

! All specifications are subject to change without notice.

C3225JB0J686M200AC



Associated Images



! Images are for reference only and show exemplary products.

! This PDF document was created based on the data listed on the TDK Corporation website.

! All specifications are subject to change without notice.